

Notice of References Cited

Application/Control No.

10/556,930

Applicant(s)/Patent Under

Reexamination

ABE ET AL.

Examiner

MAHMOUD DAHIMENE

Art Unit

1792

Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A US-6,562,523 B1	05-2003	Wu et al.	430/5
B	US-			
C	US-			
D	US-			
E	US-			
F	US-			
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	S. Wolf and R.N. Tauber, (Silicon Processing for the VLSI Era, Volume 1- Process Technology, Lattice Press, 1986)(pp 407-408)□□
V	
W	
X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.